

CIRCUIT CELLAR

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ANALOG TECHNIQUES

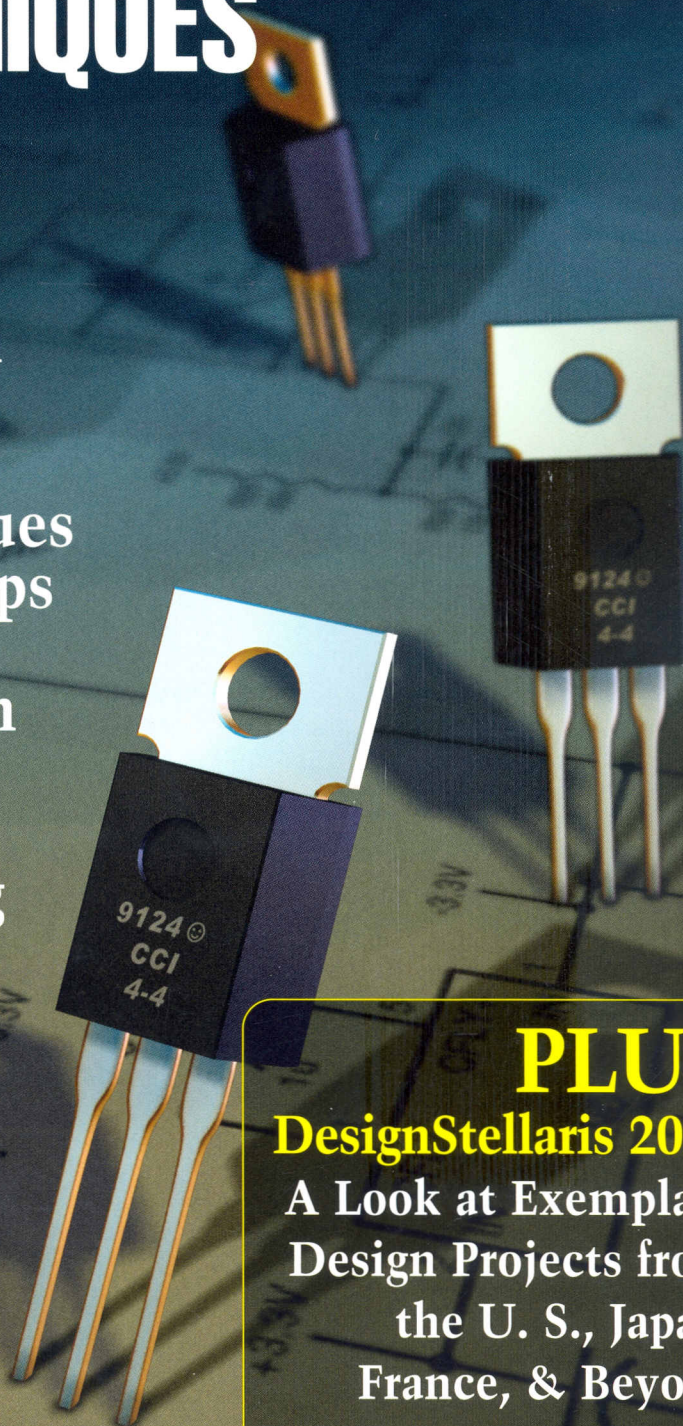
A PVDF Phased-Array
Analog Front End

Sound Generation with
Three Basic Chips

Solve Connectivity Issues
Between Embedded Apps

Digital Audio Spectrum
Analyzer

Professional Grounding
Rules



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DesignStellaris 2010

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Design Projects from
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